**COMPUTER ENGINEERING**

**TITLE OF MICRO-PROJECT: -**CHART ON FET ( FIELD –EFFECT TRANSISTOR) ADVANTAGES AND DISADVANTAGES

***GROUP MEMBERS***

|  |  |  |
| --- | --- | --- |
| **ROLL NO** | **CLASS** | **NAME OF MEMBER** |
| 1120 | CO2I | PAWAN SALVE  **(GROUP LEADER)** |
| 1116 | CO2I | AMAN PANDEY |
| 1117 | CO2I | PRIYANKA PATIL |
| 1118 | CO2I | HERAMB PAWAR |
| 1119 | CO2I | ADITYA RAUT |

**GUIDE NAME :- MRS. MONAL MALGE**

**PART A PLAN**

**TTITLE OF MICRO-PROJECT *:-*** CHART ON FET ( FIELD –EFFECT TRANSISTOR) ADVANTAGES AND DISADVANTAGES

1. **BRIEF DESCRIPTION :-**

* THE FIELD-EFFECT TRANSISTOR (FET) IS AN ELECTRONIC DEVICE WHICH USES AN ELECTRIC FIELDTO CONTROL THE FLOW OF CURRENT.
* THE MOST COMMONLY USED FET IS THE MOSFET.
* THE CMOS (COMPLEMENTARY METAL OXIDE SEMICONDUCTOR) PROCESS TECHNOLOGY IS THE BASIS FOR MODERN DIGITAL INTEGRATED CIRCUITS.
* FETS ARE USED IN SWITCHING INTERNAL COMBUSTION ENGINE IGNITION COILS, WHERE FAST SWITCHING AND VOLTAGE BLOCKING CAPABILITIES ARE IMPORTANT.
* .FET IS ALSO KNOWN AS UNIPOLAR TRANSISTOR.
* THE PROJECT CONSIST OF A CHART DISPLAYING INFORMATION ABOUT ADVANTAGES AND DISADVANTAGES OF FET.

1. **AIM OF MICRO-PROJECT:-**

THE MICRO-PROJECT AIMS AT

* STUDIED FET IN DETAILED MANNER.
* STUDIED ABOUT FET AND ITS 3 TERMINALS (SOURCE, DRAIN, GATE) .
* STUDIED ABOUT ITS ADVANTAGES AND DISADVANTAGES.
* STUDIED ABOUT ITS WORKING PRINCIPAL.
* STUDIED ABOUT CONSTRUCTION OF FET.

**3.0 ACTION PLAN :-**

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| **SR NO** | **DETAIL OF ACTIVITIES** | **PLAN START DATE** | **PLAN END DATE** | **NAME OF TEAM MEMBERS** |
| 1 | GROUP FORMED ACCORDING TO ROLL NO & GROUP LEADERS WERE ELECTED ALSO DETAILED INFORMATION ON MICRO PROJECT WAS GIVEN | 11/12/18 | 21/12/18 | AMAN PANDEY |
| 2 | FINALIZATION OF MICROPROJECT AS WELL AS DETAILED DISCUSSION REGARDING TOPIC | 24/12/18 | 4/1/19 | PRIYANKA  PATIL |
| 3 | PLANNING OF MICRO-PROJECT REGARDING MAKING OF CHART, RESOURCES REQUIRED, DECORATIONS ,SUBMISSION DATE AND COMPLETING PART A PLAN OF MICRO – PROJECT | 7/1/19 | 18/1/19 | HERAMB  PAWAR |
| 4 | COMPLETE ANALYSIS OF DESIGN PART OF MICRO-PROJECT & DISTRIBUTION OF MODULE AMONG GROUP MEMEBERS | 21/1/19 | 1/2/19 | ADITYA  RAUT |
| 5 | GETTING IT FINALIZED BY THE GUIDE | 4/2/19 | 15/2/19 | Pawan salve |
| 6 | CORRECTION OF ERRORS | 18/2/19 | 1/3/19 |  |
| 7 | PRESENTATION OF 1ST PART OF MICRO-PROJECT INFRONT OF GUIDE BY EACH GROUP MEMBER AND  PREPARING OF PART B PLAN FOR MICRO-PROJECT | 4/3/19 | 15/3/19 |  |
| 8 | SUBMISSION OF MICRO-PROJECT | 18/3/19 | 22/3/19 |  |

***4.0 RESOURCES USED :-***

|  |  |  |
| --- | --- | --- |
| **SR NO** | **NAME OF RESOURCES REQUIRED** | **QUANTITY** |
| 1 | CHART PAPER | 1 |
| 2 | SCISSORS & FEVICOL | 1 |
| 3 | DECORATIVE MATERIAL | -- |